

S68F Ver.02

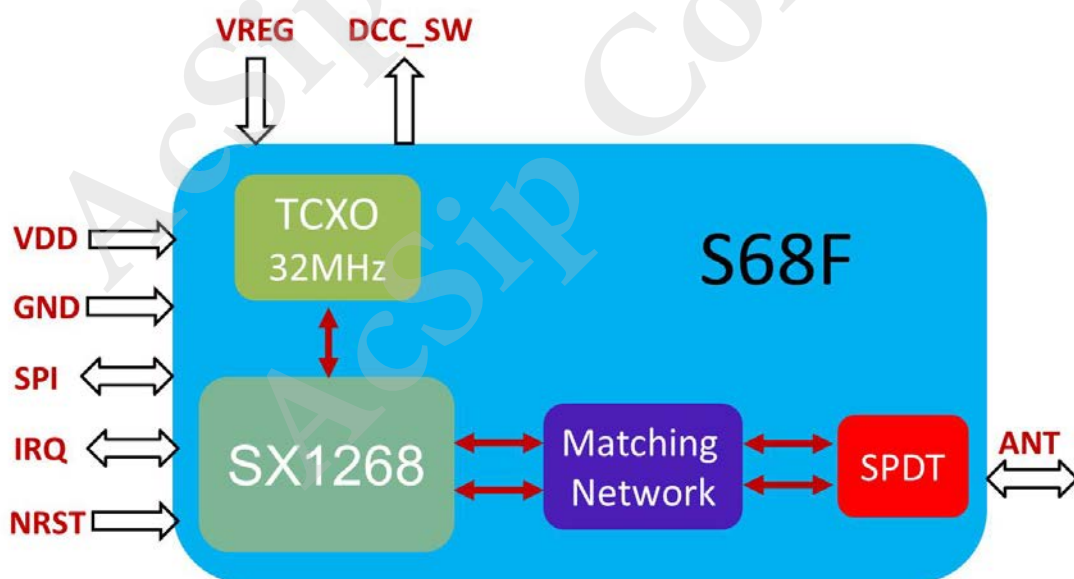
Product Brief



FEATURE

- Small footprint: 9 mm x 8 mm x 1.26 mm (Typ.)
- LoRa RF Transceiver Module
- + 22 dBm Max. RF Output Power
- High sensitivity: down to -137 dBm@BW=125kHz,SF=12
- Low power consumption
- TCXO= 32MHz embedded
- Programmable bit rate up to 62.5 kbps LoRa 、 150kbps FSK

Block Diagram



Technical Specification	
Model Name	S68F
Product Description	LoRa RF Transceiver Module
Package	LGA type
Host Interface	SPI
Dimension	9 mm x 8 mm x 1.26 mm(Typ.)
Operation Conditions	
Operating Voltage	■ 3.3V
Temperature	■ Storage : -50℃ ~ +105℃ ■ Operating : -40℃ ~ +85℃ ■ Low TX Duty cycle (*Note): -40℃ ~ +95℃
Humidity	■ Operating : 10 ~ 95% (Non-Condensing) ■ Storage : 5 ~ 95% (Non-Condensing)
Electrical Specifications	
Frequency	433MHz ~510MHz
Band Width	7.8~500KHz
Tx Power	+22dBm Max.
Sensitivity	< -137dBm@BW=125kHz, SF=12

*Note: Low power dissipation means low TX duty cycle and low GPIO driving and sinking current.

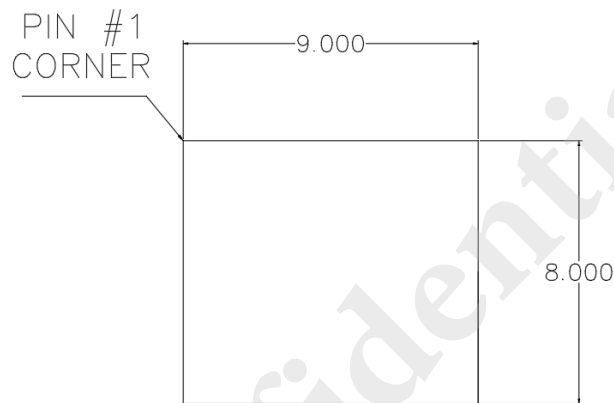
PIN Definition

Pin No.	Pin Name
1	GND
2	VREG
3	DCC_SW
4	VDD
5	DIO 1
6	BUSY
7	NRST
8	DIO2 / RF_Switch_Control
9	MISO
10	MOSI
11	SCK
12	NSS
13	RF_VC1
14	GND
15	RF_ANT
16	GND
17	RF_VC2
18	GND
19	GND
20	GND
21	GND
22	GND
23	GND
24	GND
25	EGND
26	EGND
27	EGND
28	EGND
29	EGND

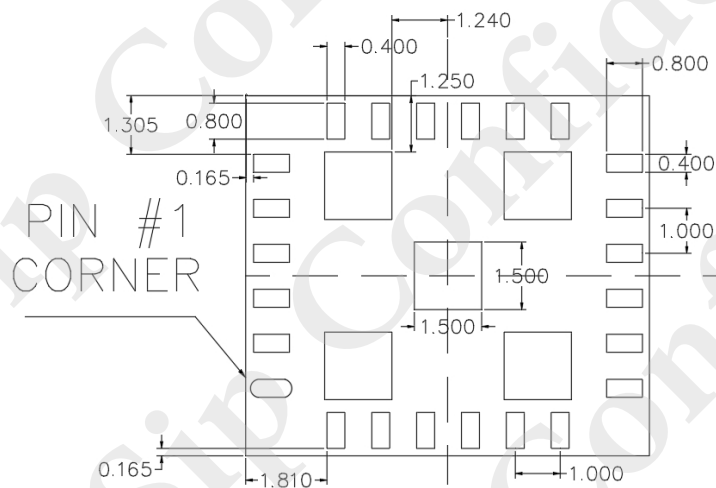
Product Brief

Mechanical Dimension(Typ.)

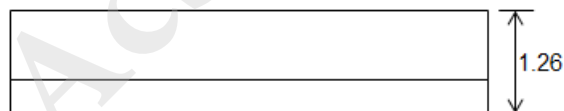
Unit: mm



Top View



Bottom View



Side View