

AI7697H/HD Bootstrap Pins

v1.0



An IoT Solution Company

AI7697H/HD Bootstrap Pins List

GPIO1 - SW Reset

Implemented by HW Reset (SYS_RST_N)

GPIO37 - Flash Access Mode

Normal mode / Recovery Mode

Pull down: Normal mode

Pull up: Recovery mode

GPIO7 & GPIO38 - XTAL Clock Mode

GPIO7 Low, **GPIO38** High: **40MHz** (v)

GPIO7 High, **GPIO38** Low: **26MHz**

GPIO7 High, **GPIO38** High: **52MHz**

GPIO6 - 32KHz Clock Mode

Pull down: Internal 32KHz mode

Pull up: External 32KHz mode (v)

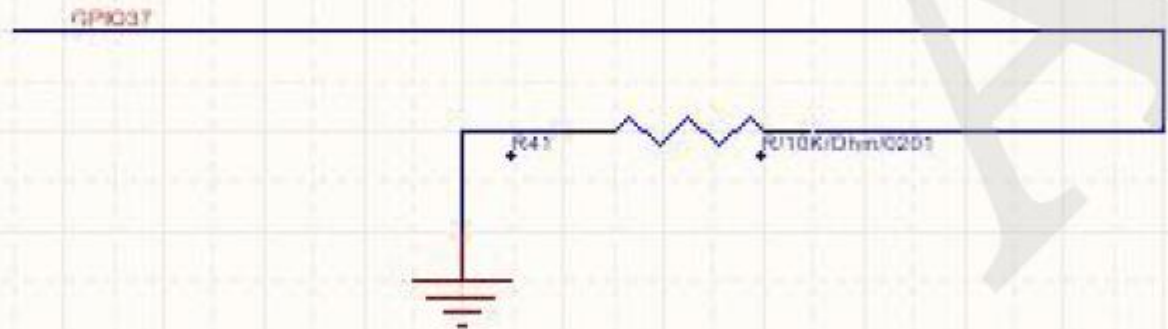
GPIO35

Pull down: Normal mode (v)

Pull up: Test mode, when in test mode, the XTAL input clock is **26MHz** only.

AI7697 GPIO37 Internal Circuit

Flash Mode Select

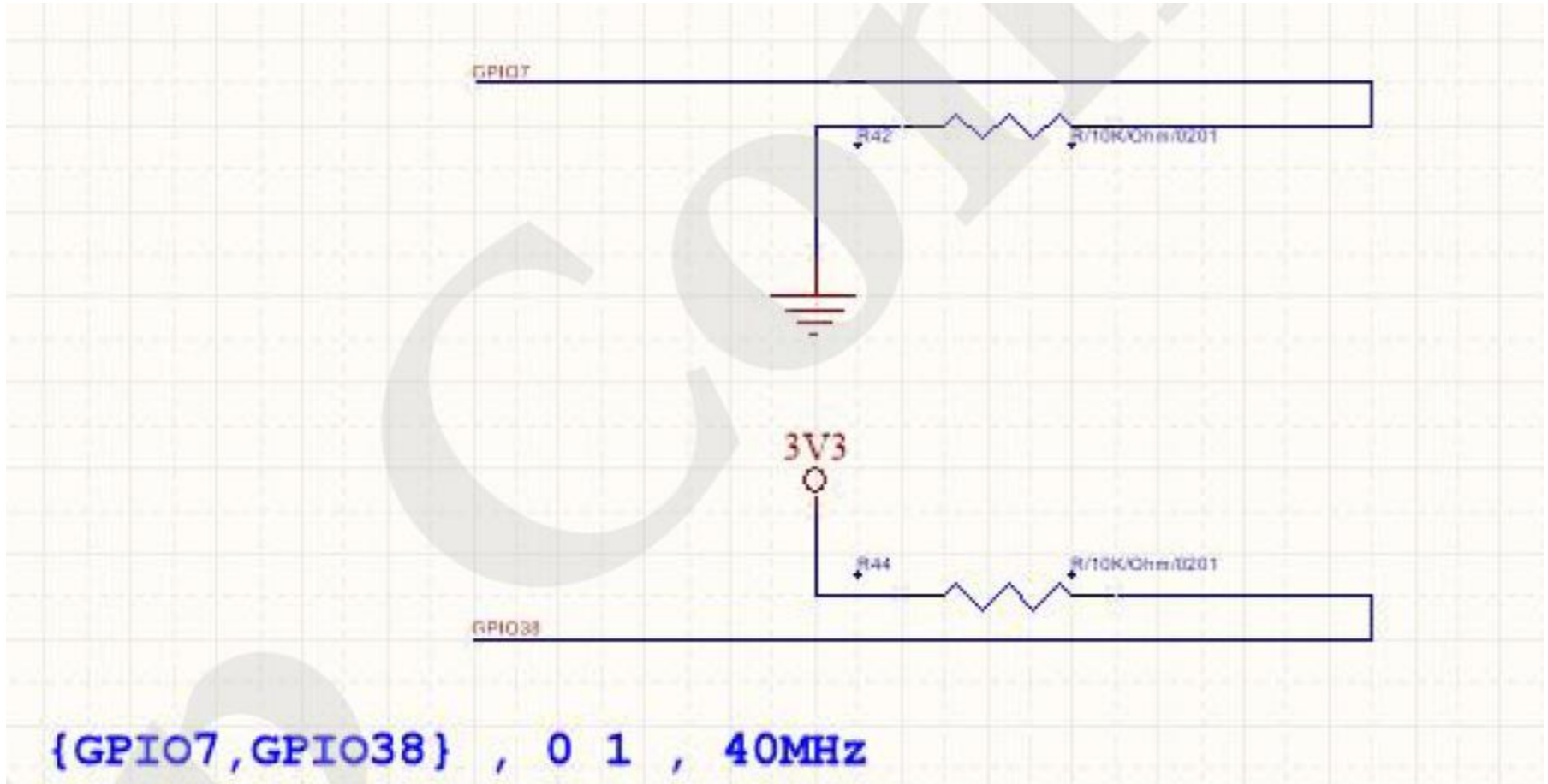


GPIO37

0 ->Flash normal mode (Default)

1 ->Flash recovery mode

AI7697 GPIO7&38 Internal Circuit



AI7697 GPIO6 Internal Circuit

32K Clock Select



GPIO6=0-->Internal 32K Clock(divide from 40MHz)

GPIO6=1-->External 32K Clock (Default, from external 32K Xtal)

AI7697 GPIO35 Internal Circuit

